PATENT ABSTRACTS OF JAPAN

(11)Publication number:

07-046198

(43)Date of publication of application: 14.02.1995

(51)Int.CI.

H04B 14/04 G11B 20/10 HO4J HO4N 7/24

(21)Application number: 05-190513

(71)Applicant : SONY CORP

(22)Date of filing:

30.07.1993

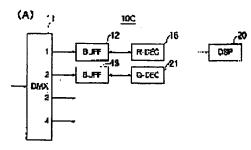
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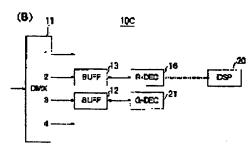
(54) DECODING METHOD AND DECODER

(57)Abstract:

PURPOSE: To decode a played back or channel-switched video signal with short start-up delay.

CONSTITUTION: A decoder 16, a pseudo decoder 21 which nullifies data accumulated in buffer memory, and buffer memory 12, 13 which receive a video signal from a de-multiplexing circuit 11 and buffer it for prescribed delay time at the front stage of the pseudo decoder 21 are provided at a decoder 10C. When channel switching from 1 to 2 is performed, the video signal of channel 2 is started to be accumulated in unused buffer memory 13, and the decoder 16 performs decoding processing on the video signal accumulated in the buffer memory 13. The pseudo decoder 21 nullifies the data accumulation state of the video signal of channel 1 accumulated in the buffer memory 12. Since the decoder 16 can perform the decoding processing even without standing by until the data stored in the buffer memory 12 is discharged even by performing the channel switching, startup delay can be reduced.





LEGAL STATUS

[Date of request for examination]

03.03.2000

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted

registration]

[Date of final disposal for application]

[Patent number]

3404808

[Date of registration]

07.03.2003

[Number of appeal against examiner's decision of

rejection